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Application Number 09/761,538

Filling Date January 16, 2001

First Named Inventor Dally, William J.

Art Unit 2663

(to be used for all correspondence after initial filing)

Mark Salvatore

Typed or printed name

Examiner Name

Chang, Richard

Total Number of	of Pages in This Submission	4	Allomey D	Jocket Number	04-0501		
		ENC	CLOSURES	S (Check	all that apply	<u></u>	
	nsmittal Form Fee Attached		Drawing(s) Licensing-relation	lated Papers			After Allowance Communication to TC  Appeal Communication to Board of Appeals and Interferences
Amendment/Reply After Final Affidavits/declaration(s)  Extension of Time Request Express Abandonment Request Information Disclosure Statement  Certified Copy of Priority Document(s)  Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53		Petition Petition to Convert to a Provisional Application Power of Attorney, Revocation Change of Correspondence Address Terminal Disclaimer Request for Refund CD, Number of CD(s) Landscape Table on CD Remarks - IDS Filling			Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)  Proprietary Information  Status Letter  Other Enclosure(s) (please Identify below):  Copy of non-patent references  Return Postcard		
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Printed name	Henry Groth			<del></del>			·
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<u> </u>				Examiner Name	<u> </u>	Chang, Ri		
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Signature	Henry 9	mtl		Registration No. Attomey/Agent)	39,696	-	Telephor	ne (408) 433-4578
Name (Print/Type)	<del></del>	<u> </u>					Date	6/30/2005

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## THE UNITED STATES PATENT AND TRADEMARK OFFICE

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e Application of:

William Dally

09/761,538

January 16, 2001

For:

Multistage Digital Cross Connect with Synchronized Configuration

Switching

Examiner: § §

Chang, Richard

Atty Docket:

**Group Art Unit:** 

1496.00407 / 04-

0501

2663

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Mark Salvatore

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## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Date: 6/30/2005

Respectfully submitted,

Henry Groth

Reg. No. 39,696

STATEMENT BY APPLICANT

**Sheet** 

INFORMATION DISCLOSURE

of

	Com	plete if Known	
	Application Number	09/761,538	
	Filing Date	January-16, 2001	
	First Named Inventor	William Dally	
	Group Art Unit	2663	
_	Examiner Name	Richard Chang	
	Attorney Docket No.	1496.00407 / 04-0501	

			U.S. PATENT DOC	UMENTS	
Examiner Cite Initials No.		114	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		5,754,120	Argentati	05-19-1998	
		5,724,351	Chao et al.	03-03-1998	

		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	
Examiner Initials	Cite No.	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, caalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where	Т
		"Call Scheduling Algorithms in a Multicast Switch", Kim et al., IEEE Transactions on Communications, Volume 40, No. 3, March 1992, pages 525-635	
		"On Rearrangeable Three-Stage Connection Networks", Benes, The Bell System Technical Journal, Volume XLI, No. 5, September 1962, pages 1481-1492	

Examiner	Date
signature	considered